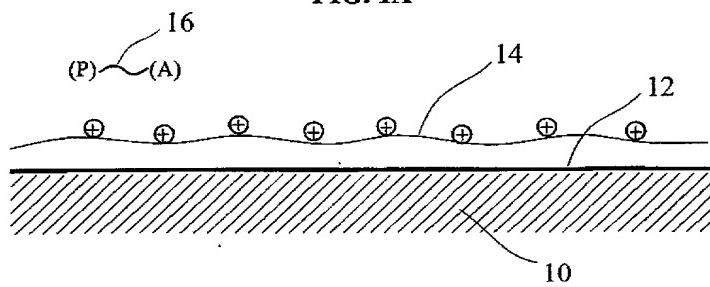
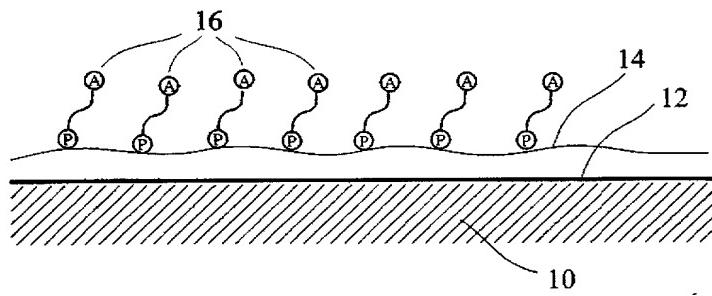


**1/2**

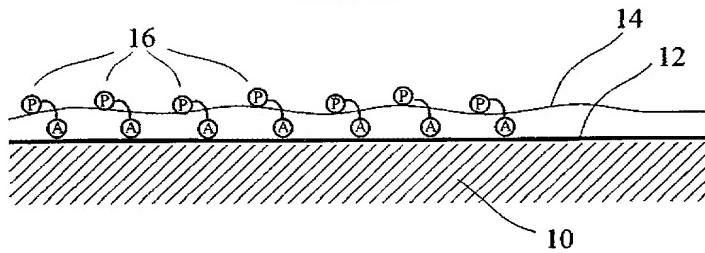
**FIG. 1A**



**FIG. 1B**

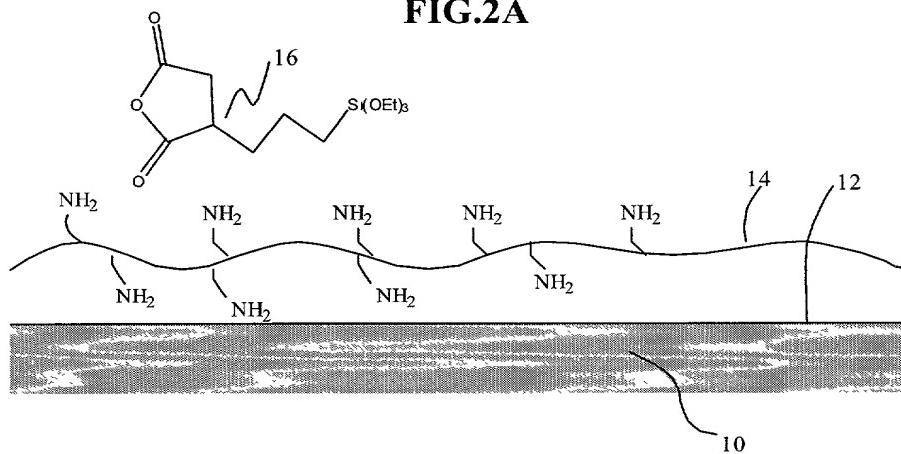


**FIG. 1C**

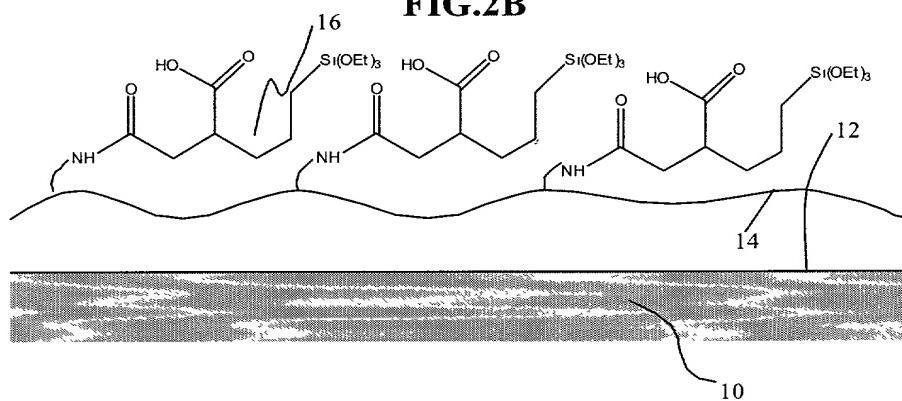


2/2

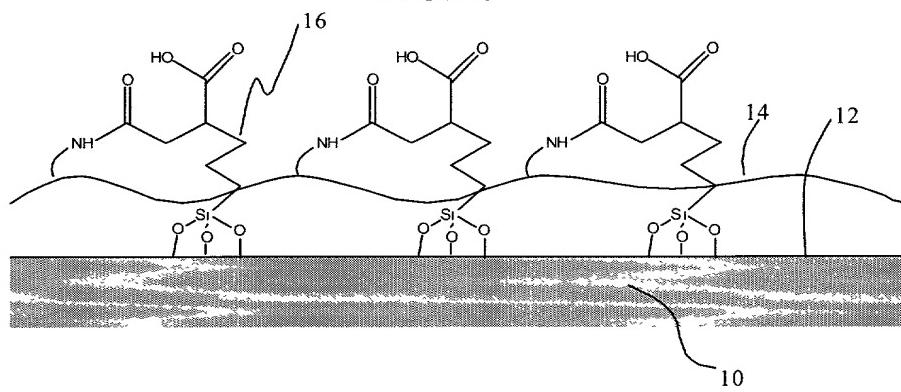
**FIG.2A**



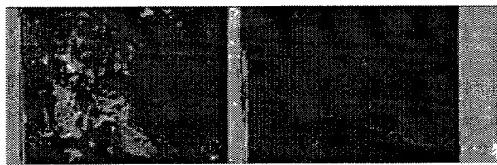
**FIG.2B**



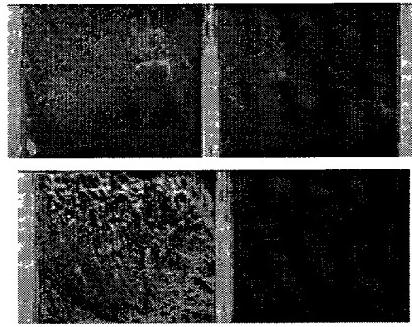
**FIG.2C**



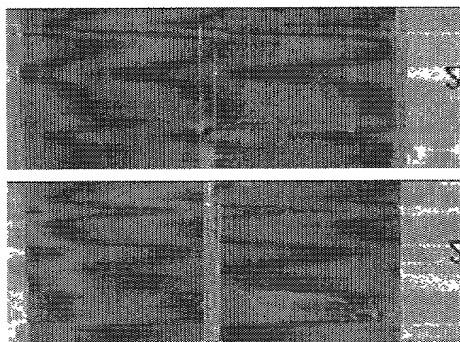
Figures3A&3B



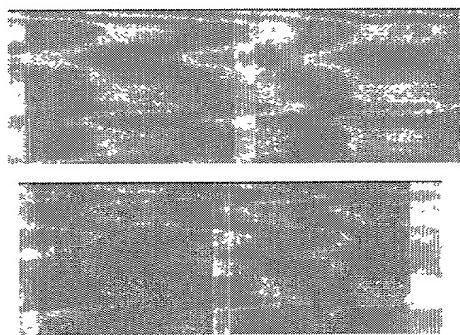
PLL slides from SOP



PLL slides from SOP/3 hour baking at 80C



PLL slides from modified SOP/3 hour baking at 80C



PLL slides from modified SOP/17 hour at 80C

Figures4A&4B